

IN THE UNITED STATES PATENT OFFICE

Applicant: Brian T. Brunn et al.

Assignee: Xilinx, Inc.

Title: Bit-Edge Zero Forcing Equalizer

Serial No.: 10/791,924 File Date: March 2, 2004

Examiner: Siu M. Lee Art Unit: 2611

Docket No.: X-1549 Conf. No.: 3818

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Mail Stop Amendment  
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AMENDMENT IN RESPONSE TO SECOND OFFICE ACTION

In response to the Second Office Action mailed from the Patent Office on November 28, 2007, please enter the amendments to the claims as indicated in the following claim listing and entertain the ensuing remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 6 of this paper.